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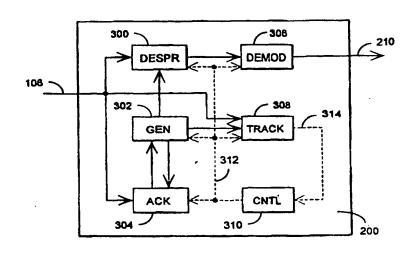
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(54) Title: METHOD FOR CONTROLLING A RECEIVER, AND A RECEIVER

(57) Abstract

The invention relates to a method for controlling a receiver and to a receiver realized with the rake principle and comprising a number of correlator branches (200-204) that can be synchronized with the received signal. In order to enable the rapid operation of the receiver, the correlator branches (200-204) of the receiver according to the invention comprise means (304) for searching for advantageous signal components and means (116, 310) for guiding a branch to receive an advantageous signal component it has found.



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Method for controlling a receiver, and a receiver

Field of the invention

The invention relates to a method for controlling a receiver realized with the rake principle and comprising a number of correlator branches that can be synchronized with a received signal.

Background of the invention

A receiver operating with the rake principle comprises several branches each of which may be synchronized with a different signal component. Therefore the receiver can receive several signals simultaneously. Rake receivers are used especially in CDMA receivers.

CDMA is a multiple access method, which is based on the spread spectrum technique and which has been applied recently in cellular radio systems, in addition to the prior FDMA and TDMA methods. CDMA has several advantages over the prior methods, for example the simplicity of frequency planning and spectral efficiency.

In the CDMA method, the narrow-band data signal of the user is multiplied to a relatively wide band by a spreading code having a considerably broader band than the data signal. In known test systems, bandwidths such as 1.25 MHz, 10 MHz and 25 MHz have been used. In connection with multiplying, the data signal spreads to the entire band to be used. All users transmit by using the same frequency band simultaneously. A separate spreading code is used over each connection between a base station and a mobile station, and the signals of the users can be distinguished from one another in the receivers on the basis of the spreading code of each user. The spreading codes are preferably selected in

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such a way that they are mutually orthogonal, i.e. they do not correlate with each other.

Correlators provided in conventional CDMA receivers are synchronized with a desired signal, which is recognized on the basis of a spreading code. The data signal is restored in the receiver to the original band by multiplying it again by the same spreading code as during the transmission. Signals multiplied by some other spreading code do not correlate in an ideal case and are not restored to the narrow band. They appear thus as noise with respect to the desired signal. The object is to detect the signal of the desired user from among several interfering signals. In practice, the spreading codes are not orthogonal and the signals of other users hinder the detection of the desired signal by distorting the received signal non-linearly. This interference caused mutually by the users is called multiple access interference.

For the performance of the spread spectrum system, it is of primary importance that the receiver can be synchronized with the incoming signal rapidly and accurately. The synchronizaton with the incoming signal usually takes place in two stages. In the acquisition of the code phase, the desired signal is to be located from the received transmission and the phase of the signal is to be determined with the accuracy of half a chip. When this has been completed, the phase is considered locked, whereafter the code phase is fine-tuned with a code tracking loop that is in charge of maintaining the phase locking.

The acquisition of the code phase can be implemented by means of either a matched filter or active correlation. The former method is fast but it is only applicable for short codes and it consumes a great deal of current when implemented digitally. Active

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correlation is the most often used method in CDMA systems. In active correlation, code phases of a local correlator are scanned in steps of half a chip and each phase is compared to the received signal. This implementation is economically advantageous but slow. The acquisition can be fastened by using several correlators in parallel so that the area of acquisition can be divided into several parts. The time of acquisition is then naturally shorter.

In prior arrangements the rake receivers have been designed in such a way that the acquisition of the new code phases takes place in a centralized manner in a separate searcher branch. The searcher branch searches for signals intended for the receiver and separate correlators are reserved for monitoring and demodulating signals that have been found. Another all correlators for is to use the arrangement acquisition when connecting to the system, and when the desired signal has been found, the operation of the correlators is invariable, i.e. one or two branches operate as searchers and the others monitor the desired signal.

The control algorithm for the branches of the rake receiver is a complicated sorting algorithm that is complex to implement as regards both software and hardware. Due to the complicated nature, calculating the control algorithm of the branches also takes up a great deal of time, which deteriorates the performance of the receiver.

Characteristics of the invention

The purpose of the present invention is to realize a receiver where the rake branches are controlled in a simple manner and the control is faster than in the conventional arrangements.

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This is achieved with a method of the type described in the preamble, characterized in that each correlator branch in the receiver searches for advantageous signal components, synchronizes itself with a signal component it has found, and that each branch monitors and receives the signal component it has found.

The invention also relates to a receiver realized with the rake principle and comprising a number of correlator branches that can be synchronized with the received signal. The receiver according to the invention is characterized in that the correlator branches of the receiver comprise means for searching for advantageous signal components and means for guiding a branch to receive an advantageous signal component it has found.

In the method according to the invention, each branch in a rake receiver searches for a signal that it starts to receive and monitor. The branches of the rake receiver start operating with different delays in order to ensure the starting of fast reception. When the branches scan the different signal delays, the levels of the signals that have been found are compared either to a predetermined or an adaptive threshold value. This ensures that only a signal that is of sufficiently high quality is selected for reception. The arrangement according to the invention does not require a separate searcher block or a block calculating the allocation of Especially the calculation branches. allocation is complicated and requires a considerable amount of software or hardware and takes up a relatively long time.

Description of the figures

In the following, the invention will be described in greater detail with reference to the examples according to the accompanying drawings, in which

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Figure 1 is a general block diagram illustrating a receiver according to the invention in a spread spectrum system,

Figure 2 illustrates in greater detail the structure of a detector in a receiver according to the invention, and

Figure 3 is a block diagram illustrating an example of the structure of a branch in a rake receiver according to the invention.

Description of the preferred embodiments

Examine at first a typical CDMA signal. Assume in the following example that the system uses BPSK modulation, without restricting the invention thereto, however. The signal to be transmitted is multiplied both with a spreading code and a carrier wave $\cos\left(w_{0}t\right)$. Assume that the duration of a bit (chip) in the spreading code is T_{c} . When the signal is then multiplied with a spreading code c(t) having a frequency that is considerably greater than the frequency of the information signal, the spectrum of the signal to be transmitted spreads to a band $1/T_{c}$. The signal on the channel is delayed, wherefore the received signal can be set forth in the following form

25. $a(k) * c(t-\tau)cos(\omega_0 t),$

wherein a(k) = binary data (±1), c(t) = time-continuous spreading code $\omega_0 = \text{carrier wave angular frequency}$ $\tau = \text{transit time delay.}$

In the receiver, the received signal must be composed back to a narrow band. To enable this, the receiver must have the same spreading code as the transmitter and the code should also have the same phase. The function in the reception is to estimate the

transit time delay τ . The information signal is obtained by multiplying the received signal with a spreading code having the correct phase according to the following formula

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$$a(k) * c(t-\tau)c(t-\tau')cos(\omega_0t)$$
,

wherein τ' is the estimate for the transit time delay. If the spreading code generated by the receiver has exactly the correct phase, the signal that can be composed is $a(k)\cos(\omega_0 t)$ which equals the original carrier and data-modulated signal. If the phase of the spreading code generated in the receiver deviates for example 0.5 chip, in the case of BPSK the performance of the receiver deteriorates 6 dB. Rapid and exact phase

acquisition is therefore essential.

Examine next the general structure receiver according to the invention by means of the block diagram of Figure 1. The figure shows a receiver of a subscriber equipment, but the receiver according to the invention may naturally also be located in a base station and the essential parts of the receiver, i.e. especially the detection blocks, are implemented in the same manner as in the terminal equipment. The receiver according to the invention comprises an antenna 100 receiving a signal that is applied to radio-frequency parts 102 wherein the signal is converted into an intermediate frequency. From the radio-frequency parts the signal is applied to an analog-to-digital converter 104 wherein the signal is sampled and converted into a digital form. The converted signal 106 is applied to a detection block 108 where the channel parameters of the signal and the desired symbols contained in the signal are detected.

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In the terminal equipment receiver shown in Figure 1, the detected signal is applied to a channel decoder 110 and a speech decoder 112, whereafter the decoded speech signal is applied to a loudspeaker 114. If the equipment is a data terminal equipment, the speech decoder is naturally replaced with some other decoder. Further, if the receiver is a base station receiver, the signal is applied after the channel coding block to other parts of the receiver. The receiver according to the invention further comprises a control processor 116 that controls the operation of the other parts. The receiver according to the invention naturally also comprises other components, such as filters and amplifiers, as it is clear for a person skilled in the art, but they are not shown in the figure for the sake of clarity.

Examine in greater detail the structure of the detector 108 in a receiver implemented with the rake principle by means of the block diagram of Figure 2. The detector 108 comprises a number of rake branches 200 to 204 the input of which is the received signal 106 and — each of which monitors and demodulates a separate multipath-propagated signal component. The outputs 210 to 214 of the branches 200 to 204 are applied to a diversity combiner 206 wherein the demodulated components can be preferably combined and diversity gain is thus obtained.

In the arrangement according to the invention, each branch independently searches for signal components with which it could be synchronized, and after finding a signal component fulfilling the criteria, it starts monitoring and receiving the component. The criteria for the reception may be the power level of the signal that is compared to a predetermined reference value, which may be a fixed or dynamically changing reference level.

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When a signal exceeding the reference level has been found, the branch makes sure that no other receiver branch already receives the detected component. If the phase is already being received, the branch continues the acquisition of the signal from the following code phases.

The branches of the rake receiver receive and monitor signal components that are within a given socalled time window, i.e. that have delays within a certain interval. The time window to be received is set to a predetermined length. Adaptivity is also possible in adjusting the time window. The place of the time window is set for the first time when the first rake branch finds a signal that can be received. This may take place for example in such a way that the branch sets the initial and final phase of the time window in registers that are visible to all'branches and that may be located for example in the control unit 116. The place of the time window is adjusted later in such a way that when the next rake branch finds itself a signal, it checks the availability of the component found, i.e. it examines whether the signal is received by any other branch, and when the delay is available, it updates the initial and final phase of the time window in the registers, for example by centring the time window on the code phases found.

The branches of the rake receiver receive the signal component they have found as long as possible. The branch must stop receiving the signal either when it deteriorates below the reference value or if the component is left outside the time window to be received due to changes in the components of the other branches. The branch that has lost its signal then searches for a new signal component from the time window that is

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received until it finds a component that fulfils the criteria.

After having found a signal component, each rake branch therefore always checks that the component is not used by some other branch. Each branch supplied with its own clock signal 216 that synchronized in such a way that each branch has its own reading and updating the required slot for registers. The clock signal controls the checking of the the other branches and the reading phases of window registers. Due of the time to updating synchronization, each branch reads the registers at a different time, and therefore the registers are updated in a controlled manner and the branches do not update the time window independently of or unknown to the other branches.

Figure 3 illustrates in greater detail the essential parts of a rake branch in a receiver according to the invention. It is clear for a person skilled in the art that the branch also comprises other blocks, such as the estimation of the complex coefficient of the multipath-propagated signal, but they are not shown in the figure since they are not essential to the present invention.

A rake branch in a receiver according to the invention comprises firstly a code generator 302 that generates a desired spreading code having a desired phase. The spreading code is applied to correlation means 300 that can be implemented according to known technology for example by means of a correlator or a matched filter. In the correlation means, the received signal 106 is multiplied with a spreading code that is obtained from the code generator 302 and that restores the desired signal to the original narrow band if the code has the correct phase. The obtained signal is then

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applied further to a demodulator 306 where the signal can be demodulated according to known technology. The demodulated signal 210 is further applied to other parts of the receiver.

The rake branch further comprises monitoring means 308 the input of which consists of the received signal 106 and the output signal of the code generator 302. The code-monitoring means 308 monitor changes in the phase of the signal component to be received and control the phase of the code generator so that it has the same phase as the desired signal The branch further comprises a control component. processor 310 that controls the operation of the branch. The control processor also receives information 314 from the code-monitoring means if the synchronization with the desired signal component is lost. Acquisition for a new signal component should then be started. The control processor 310 also checks from the registers of the control unit 116 in the receiver whether the signal component found is already received by some other branch. The control processor also manages the checkings and updatings of the time window register. At the beginning of a connection, the control processor 310 of each branch also phases the branch by means of the control unit 116 so that the phases of the different branches 200 to 204 of the receiver are different. It is therefore possible to speed up the finding of a new signal component.

The branch further comprises means 304 for searching for new signal components from the received signal 106. The acquisition is started by a command from the control processor 310 when the phase of the previously monitored component is lost or if the level of the monitored component drops below a given threshold value. In the receiver according to the invention, the

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acquisition means 304 may also search for new signal components during the reception as a kind of background process. The advantage is that when the previously monitored component is lost, it is possible to immediately synchronize with a new component the phase of which has been detected as a result of the background acquisition. Both the code-monitoring means 308 and the acquisition means 306 can be implemented for example by means of correlators or matched filters.

If the receiver according to the invention is used in a base station and it is implemented in such a way that separate rake branch cards are used, the time window registers can be placed in one of the rake branches instead of the control unit 116. Further, the functions of the receiver according to the invention can be implemented in full by means of hardware for example with ASICs to obtain a compact implementation, or also by means of hardware and software.

Even though the invention is described above with reference to the example according to the accompanying drawings, it is clear that the invention is not restricted thereto, but it can be modified in many ways within the scope of the inventive idea disclosed in the appended claims.

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Claims:

1. A method for controlling a receiver realized

with the rake principle and comprising a number of correlator branches (200 - 204) that can be synchronized with a received signal, c h a r a c t e r i z e d in that

each correlator branch (200 - 204) in the receiver searches for advantageous signal components,

synchronizes itself with a signal component it has found, and that

each branch monitors and receives the signal component it has found.

- 2. A method according to claim 1, c h a rac t e r i z e d in that after it has found an advantageous signal component, each branch checks whether another branch receives the same signal component.
- 3. A method according to claim 2, c h a rac t e r i z e d in that at the beginning of the reception, the correlator branches of the receiver start searching for advantageous signal components with different signal delays.
- 4. A method according to claim 1, c h a rac t e r i z e d in that in the acquisition of the signal component, the quality of each component found is compared to a given threshold value.
- 5. A method according to claim 2 and 4, c h a r a c t e r i z e d in that the correlator branch starts receiving the signal component it has found, if the quality of the component exceeds the given threshold value and if no other correlator branch receives said component.

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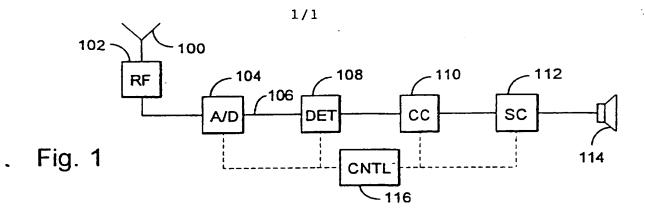
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- 6. A method according to claim 1, c h a rac t e r i z e d in that the correlator branches (200 204) are phased in such a way that each branch searches for signals having different phases.
- 7. A method according to claim 1, c h a rac t e r i z e d in that when they monitor and receive an advantageous signal component, the correlator branches (200 204) simultaneously search for new signal components.
- 8. A method according to claim 4, c h a rac t e r i z e d in that after having lost the signal component they were monitoring, the correlator branches (200 204) search for a new signal component exceeding the threshold from the given delay range.
 - 9. A receiver realized with the rake principle and comprising a number of correlator branches (200 204) that can be synchronized with the received signal, c h a r a c t e r i z e d in that the correlator branches (200 204) of the receiver comprise means (304) for searching for advantageous signal components and means (116, 310) for guiding a branch to receive an advantageous signal component it has found.
 - 10. A receiver according to claim 9, c h a rac t e r i z e d in that the correlator branches (200 204) of the receiver comprise means (116, 310) for checking whether another receiver branch receives the signal component it has found.
 - 11. A receiver according to claim 9, c h a rac t e r i z e d in that the correlator branches (200 204) of the receiver comprise means (116, 310) for comparing the quality of the signal component found to a given threshold value.
 - 12. A receiver according to claim 9, c h a ra c t e r i z e d in that the receiver comprises means (116) for phasing the correlators used for the

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acquisition so that each correlator searches for signals with different phases.

13. A receiver according to claim 9, c h a rac t e r i z e d in that the receiver comprises means (304) for searching for new signal components simultaneously with monitoring and receiving an advantageous signal component.



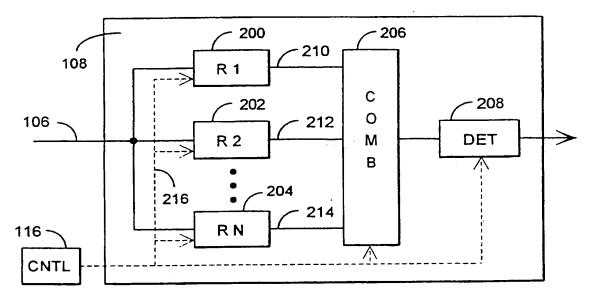
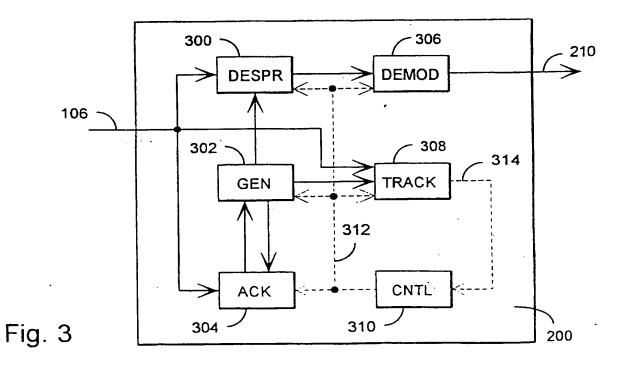
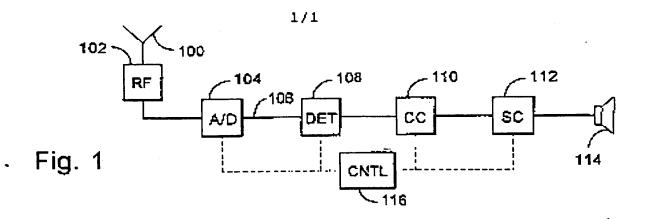


Fig. 2



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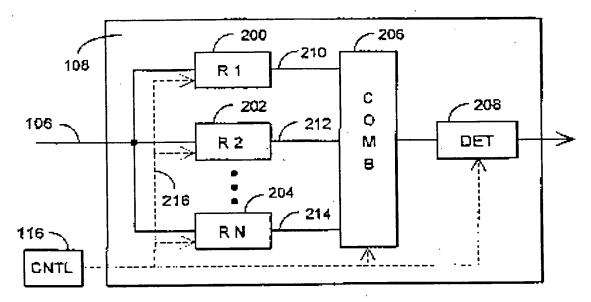
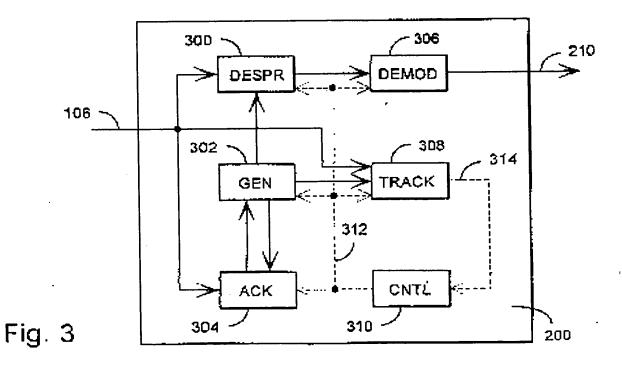


Fig. 2



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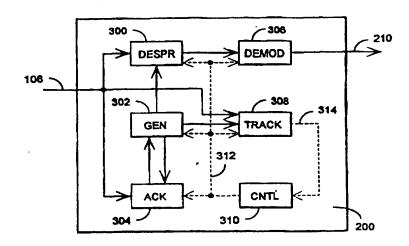
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(57) Abstract

The invention relates to a method for controlling a receiver and to a receiver realized with the rake principle and comprising a number of correlator branches (200-204) that can be synchronized with the received signal. In order to enable the rapid operation of the receiver, the correlator branches (200-204) of the receiver according to the invention comprise means (304) for searching for advantageous signal components and means (116, 310) for guiding a branch to receive an advantageous signal component it has found.



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Category*	Citation of document, with indication, where app	ropriate, of the relevant passages	Relevant to claim No.				
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20 May		28 -05- 1997					
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